

A >> 7/13/2016 >> INITIAL RELEASE TO PRODUCTION
B >> 8/8/2016 >> UPDATED PER ADC REQUEST
C >> 2/7/2017 >> CHANGE 'BX-08CDR' TO 'BX-08CD3R' CHANGE 'Y / 4,5,6,7' TO 'Y / 0,1,2,3'

ZL-RTB20 CUT-OUT LABEL DIMENSIONS

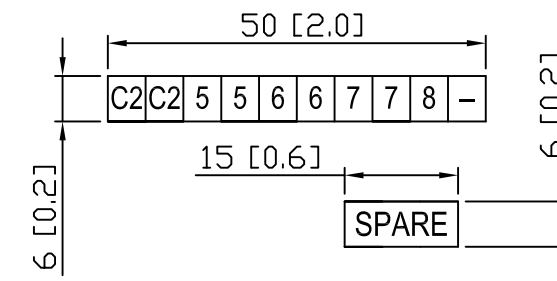


Diagram illustrating a 64-bit register structure with 16 fields. The register is divided into two 32-bit halves. The left half contains 8 fields: C2, C2, 5, 5, 6, 6, 7, and 7. The right half contains a SPARE field followed by an empty field. Dimensions are given in bits and bytes: the first field is 6 bits (0.2 bytes), the next two are 40 bits (1.6 bytes), the next two are 15 bits (0.6 bytes), and the last two are 6 bits (0.2 bytes).

COMPANY: FACTS ENGINEERING LLC
DATE: JULY 13, 2016
DESCRIPTION: ZIPLINK TERMINAL BLOCK LABELS
PART NUMBER: ZL-BXEM-CBL10-FP
REVISION: C